

METHOD OF VERIFYING A MASK FOR A MASK ROM

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ABSTRACT OF THE DISCLOSURE

10 In a method to verify a mask for a mask ROM, a
serial of random codes that are exclusive to each other are
implanted into a plurality of wafers manufactured by a same
process with the mask or a plurality of die regions in a single
wafer manufactured by a same process with the mask, and
then the test results derived from the implanted wafers or die
regions are compared to determine if the mask is defective.

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